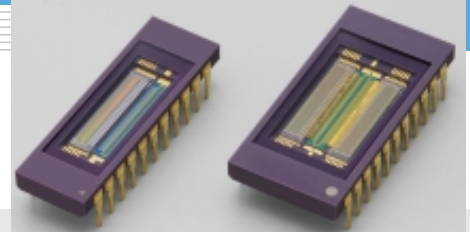


InGaAs linear image sensor G9494-256D/-512D

Near infrared image sensor (0.9 to 1.7 μm) with high-speed data rate



HAMAMATSU provides high-speed, near infrared image sensors designed for detectors used in on-line foreign object inspection equipment. These image sensors use square-shaped pixels that are ideally suited for software processing in pattern recognition systems. The signal processing circuit employs CTIA (Charge Transfer Impedance Amplifier) method to perform simultaneous charge integration on all pixels, so there is no time lag among the pixel output signals.

Features

- High-speed data rate: 2 MHz Typ.
- Selectable conversion gain (Cf=0.1 pF, 1 pF)
- Pixel size
G9494-256D: 50 \times 50 μm
G9494-512D: 25 \times 25 μm
- CMOS readout circuit incorporated
- Low dark current
- Room temperature operation

Applications

- Foreign object detection monitors
- Near infrared spectroscopy

■ Selection guide

Type No.	Cooling	Number of pixels	Pixel pitch (μm)	Pixel size [μm (H) \times μm (V)]	Spectral response range (μm)	Defective pixels
G9494-256D	Non-cooled	256	50	50 \times 50	0.9 to 1.7 (25 $^{\circ}\text{C}$)	Less than 1 %
G9494-512D		512	25	25 \times 25		

■ Absolute maximum ratings

Parameter	Symbol	Value	Unit
Clock pulse voltage	V ϕ max.	5.5	V
Operating temperature *1	T _{opr}	-20 to +70	$^{\circ}\text{C}$
Storage temperature *1	T _{stg}	-20 to +85	$^{\circ}\text{C}$

*1: No condensation

■ Electrical characteristics (Ta=25 $^{\circ}\text{C}$)

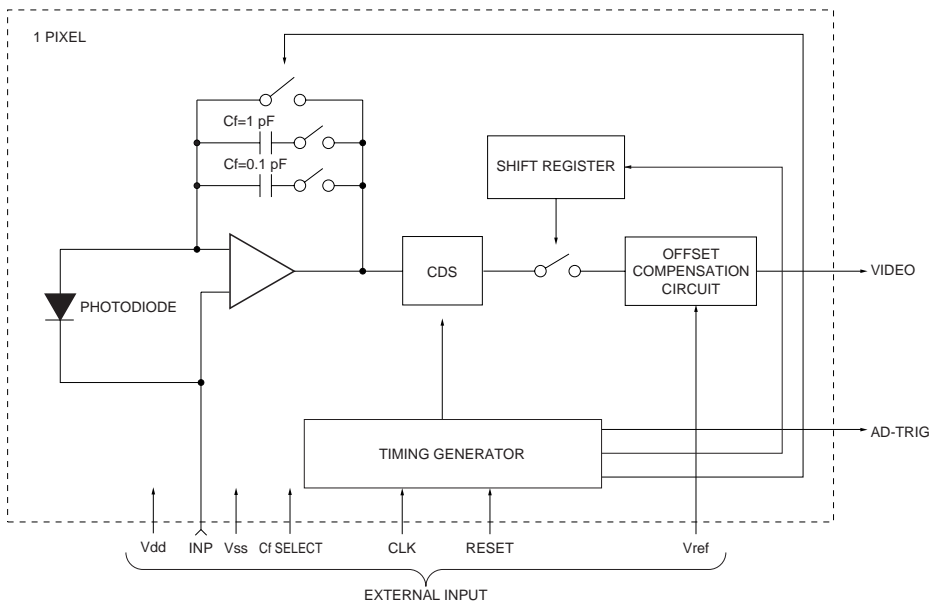
Parameter	Symbol	Min.	Typ.	Max.	Unit	
Supply voltage	V _{dd}	G9494-256D	4.5	5.0	5.5	V
		G9494-512D	4.5	5.0	5.5	
Supply voltage	V _{ref}	-	1.26	-	V	
Supply current	I _{vdd}	G9494-256D	-	40	75	mA
		G9494-512D	-	80	150	
Supply current	I _{vref}	-	-	1	mA	
	I _{INP}	-	-	1		
Ground	V _{ss}	-	0	-	V	
Element bias	INP	3.3	3.5	3.6	V	
Clock frequency	f	0.1	2	-	MHz	
Clock pulse voltage	V ϕ	High	4.5	5.0	5.5	V
		Low	-	-	0.4	
Clock pulse rise / fall times	t _{rϕ} , t _{fϕ}	0	20	100	ns	
Clock pulse width	t _{pϕ}	200	-	-	ns	
Reset pulse voltage	V (RES)	High	4.5	5.0	5.5	V
		Low	-	-	0.4	
Reset pulse rise / fall times	t _r (RES), t _f (RES)	0	20	100	ns	
Reset pulse width *2	t _{pϕ} (RES)	6/f	-	-	μs	
Video output voltage	V _H , V _L	High	-	3.5	INP	V
		Low	-	1.26	-	
Video output impedance	Z _v	-	5	-	k Ω	
A/D trigger voltage	V _{trig}	High	-	V _{dd}	-	V
		Low	-	GND	-	
A/D trigger drive function	V _{ad}	74HC244 1 ch	-	-	-	
Data rate	f _v	-	f	-	Hz	

*2: 6 μs or more

■ Specification (Ta=25 °C, Vdd=5 V, INP=3.5 V, Vref=1.26 V, Cf=0.1 pF, integration time 200 μs, per 1 element)

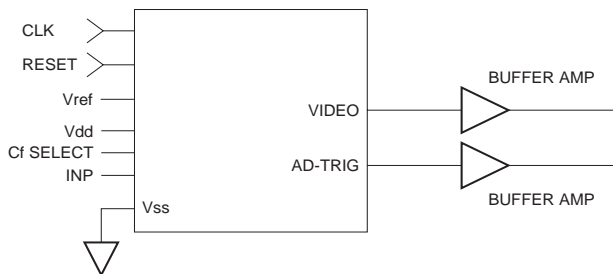
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Peak sensitivity wavelength	λ_p		-	1.55	-	μm
Saturation charge	Qsat	$V_{\phi}=5\text{ V}$	-	0.2	-	pC
Dark current	256 ch	ID	-	4	20	pA
	512 ch		-	1	5	
RMS noise voltage (readout noise)	N	Standard deviation Number of integration 50	-	900	2000	μV rms
Saturation voltage	Vsat		-	2	-	V
Photo response non uniformity	PRNU		-	±5	±20	%
Detective pixel	-		-	-	1	%

■ Equivalent circuit



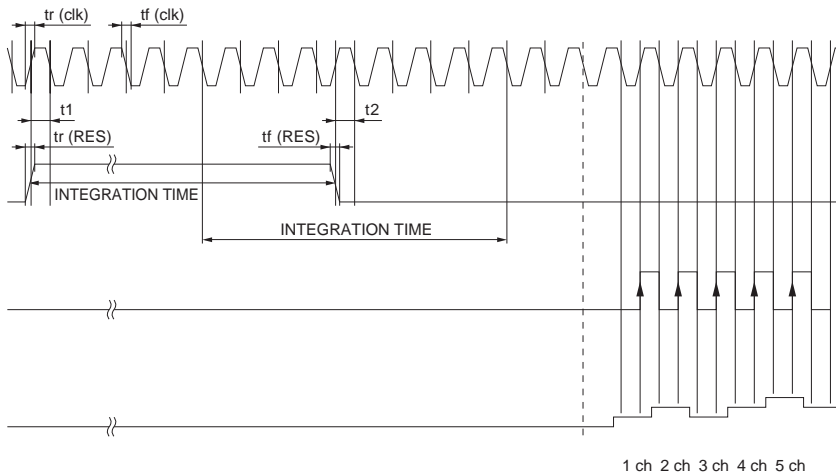
KMIRC0027EA

■ Basic circuit connection



KMIRC0012EA

■ Timing chart



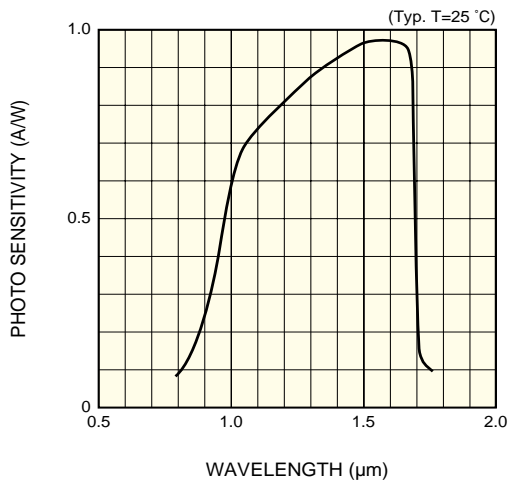
KMIRC0025EA

Note) Three clock pulses are required for a period from the last pixel to the rising edge of RESET pulse.

Parameter	Symbol	Min.	Typ.	Max.	Unit
Clock pulse frequency	-	0.1	2	-	MHz
Clock pulse width	tpw (clk)	200	-	-	ns
Clock pulse rise / fall times	tr (clk), tf (clk)	0	20	100	ns
Reset pulse width ^{*3}	tpw (RES)	6/f	-	-	μs
Reset pulse rise / fall times	tr (RES), tf (RES)	0	20	100	ns
Clock pulse to start pulse timing	t1, t2	50	-	-	ns
Video delay time	tvd	100	-	-	ns

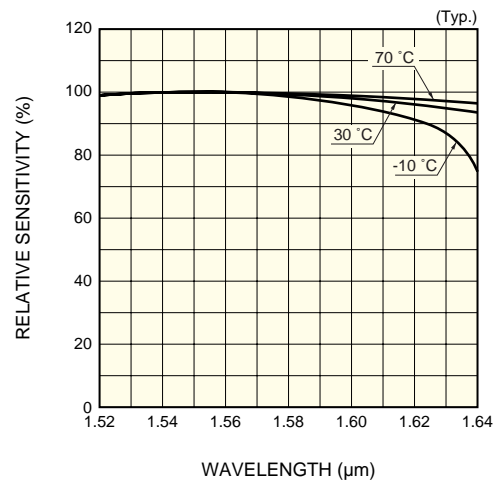
*3: 6 μs or more

■ Spectral response



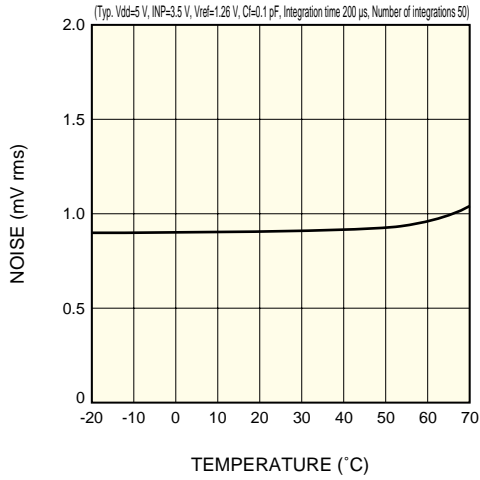
KMIRB0019EA

■ Photo sensitivity temperature characteristics



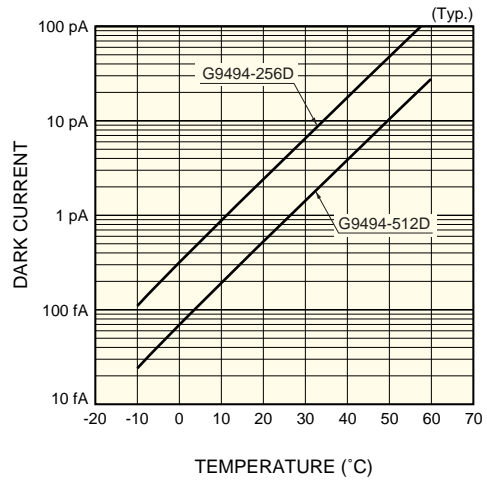
KMIRB0020EA

■ Noise vs. temperature



KMIRB0036EA

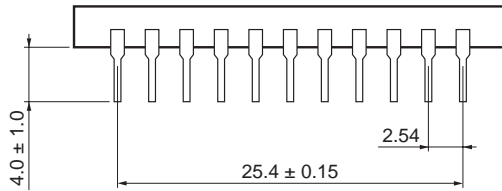
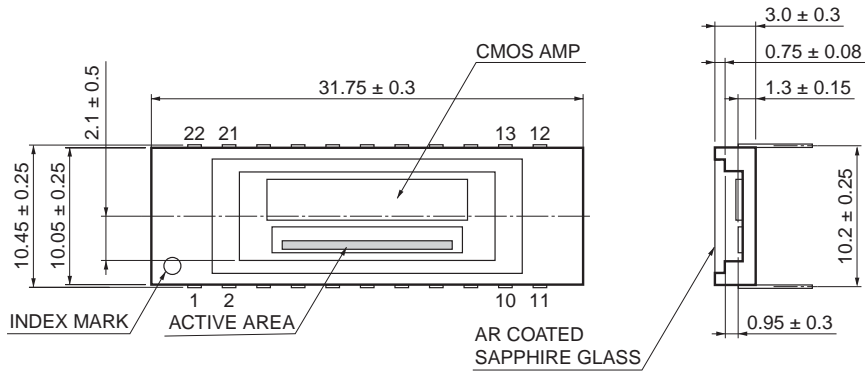
■ Dark current vs. temperature



KMIRB0035EA

■ Dimensional outlines (unit: mm)

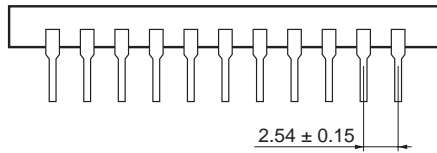
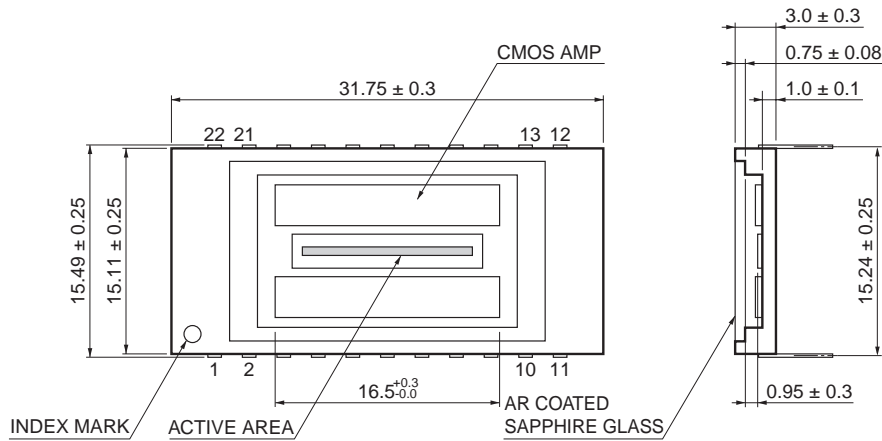
G9494-256D



Pin No.	Pin connection	Pin No.	Pin connection
1	NC	12	VIDEO
2	NC	13	V _{ref}
3	NC	14	CLK
4	NC	15	NC
5	NC	16	INP
6	NC	17	V _{SS}
7	NC	18	V _{DD}
8	NC	19	NC
9	NC	20	AD-TRIG
10	NC	21	RESET
11	NC	22	CI-SELECT

KMIRA0015EA

G9494-512D



Pin No.	Pin connection	Pin No.	Pin connection
1	NC	12	VIDEO-ODD
2	RESET-EVEN	13	Vref
3	AD-TRIG-EVEN	14	CLK-ODD
4	NC	15	NC
5	NC	16	INP
6	NC	17	Vss
7	NC	18	Vdd
8	NC	19	NC
9	CLK-EVEN	20	AD-TRIG-ODD
10	NC	21	RESET-ODD
11	VIDEO-EVEN	22	Cf-SELECT

KMIRA0016EA