

- Quick Review
- Progress on Setting Standards
- Immediate Issues
 - Continue identifying topic “contributors” this week
 - Will call small meetings to introduce folks to coding environment
 - Finalize coding standards
 - Next meeting?

- *Application-based* scenario
 - Most general
 - Greatly reduces need for shared memory
 - Matches up well with recently understood CESR Control System Limitations
 - User has full control of configuration parameters
 - Utilizes slightly generalized form of existing *command interface*
 - Requires
 - Allocation Server
 - Timing System Server
 - Watchdog
 - Constants management scheme
- DSPs configured as automatic *data servers*
 - Simplified basic data access
 - XBus vector operations
 - Requires updated drivers
 - Takes full advantage of all DSP cycles

- API update from CRS
- Data Server update from MAP
- XBus driver issues from CRS
- Other???

- **April 8:**
 - Tentatively define the DAQ hierarchy that we want to implement.
 - Assign coordinator(s) for each segment of the DAQ hierarchy.
 - Identify "contributors" for each segment.
- **April 11-15:**
 - Introduce coordinators to the coding environment.
- **April 18-29:**
 - Develop and test basic implementation examples.
- **April 29:**
 - Review trial implementations and interface issues.
- **May 2-6:**
 - Document interfaces and all required code elements to complete implementations.
 - Review and distribute on May 6.
- **May 9-Jun 11:**
 - Develop and test prototype DAQ implementations.
- **June 13-24:**
 - Prototype system-level testing during machine studies after summer down.
- **August 8-12:**
 - Final system testing during CESR-c/CLEO-c transition machine studies.